AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111 Page 2
Serial Number: 10/602,509 Dkt: 80107.023US1 (INTEL)

Serial Number: 10/602,509 Filing Date: June 24, 2003 Title: DYNAMIC TLB LOCKING

Assignee: Intel Corporation

IN THE SPECIFICATION

Please cancel the previous abstract and replace it with the following:

Abstract

A translation look-aside buffer (TLB) has lockable entries. A number of entries to lock may be determined by counting unique page access instances during an active period of a process, determining a value of a page usage metric for the process, and comparing the value of the page usage metric to values of page usage metrics for other processes. The page usage metric may consider many different factors, including the amount of time a process is active, a frequency of invocation of the process, and a priority level of a process.